

SIR CHHOTU RAM INSTITUTE OF ENGG. & TECH.

TIME-TABLE

Deptt. : Electronics & Communication Engg.

Semester : 3rd

Room No. : (EC - 207)

Class Counselor: Er. Pankaj Kumar

From: To:	09:30 AM to 10:30 AM	10:30 AM to 11:30 AM	11:30 AM to 12:30 PM	12:30 PM to 01:30 PM	01:30 PM to 02:30 PM	02:30 PM to 03:30 PM	03:30 PM to 04:30 PM	
Monday	NAS (Dr. P.K.)	Human Values (Dr. Kumkum)	Maths-IV (Dr. K.P.S)	Computer System Security (Er. K.B.)	LUNCH	mini project	Electronics Devices Tutorial (Er. PV)	Er. K.B.) : Kavi Bhushan , Er. V.R. : V.K. Ram , Dr. P.K. : Pankaj kumar,Er. Priyanka Singh, Er. P.V: Parul Varshney, Dr. Kumkum, Dr. K.P.Singh , Mini project : All faculty
Tuesday	NAS (Dr. P.K.)	Human Values (Dr. Kumkum)	Maths-IV (Dr. K.P.S)	Computer System Security (Er. K.B.)		mini project	Digital System Design (Er. PR)Tutorial	
Wednesday	NAS (Dr. P.K.)	Electronic Devices (Er. P.V.)	Maths-IV (Dr. K.P.S)	Computer System Security Tutorial (Er. K.B.)		Network Analysis and Synthesis Lab G1 (Dr. P.K)	Network Analysis and Synthesis Lab G1 (Dr. P.K)	
Thursday	NAS (Dr. P.K.)Tutorial	Digital System Design (Er. PR)	Maths-IV (Dr. K.P.S)Tutorial	Human Values tutorial (Dr. Kumkum)		Network Analysis and Synthesis Lab G2 (Dr. P.K)	Network Analysis and Synthesis Lab G2 (Dr. P.K)	
Friday	Electronic Devices (Er. P.V.)	Digital System Design (Er. PR)	Electronics Devices Lab G1(Er. PV)	Electronics Devices Lab G1(Er. PV)		Digital System Design Lab G1(Er. PS)	Digital System Design Lab G1(Er. PS)	
Saturday	Electronic Devices (Er. P.V.)	Digital System Design (Er. PR)	Electronics Devices Lab G2(Er. PV)	Electronics Devices Lab G2(Er. PV)		Digital System Design Lab G2(Er. PS)	Digital System Design Lab G2(Er. PS)	

SIR CHHOTU RAM INSTITUTE OF ENGG. & TECH.

TIME-TABLE

Deptt. : Electronics & Communication Engg.

Semester : 5th

Room No. : (EC - 207)

Class Counselor: Er. Parul varshney

From: To:	09:30 AM to 10:30 AM	10:30 AM to 11:30 AM	11:30 AM to 12:30 PM	12:30 PM to 01:30 PM	01:30 PM to 02:30 PM	02:30 PM to 03:30 PM	03:30 PM to 04:30 PM	
Monday	Electronic Switching (Er. P.V)	Digital Signal Processing (Dr. P.K.)	VLSI Technology (Er. V.R.)	Integrated Circuits (Er. R.P.)	LUNCH	DSP Lab G1(Er. P.K.)	DSP Lab G1 (Er. P.K.)	Er. V.R. : V.K. Ram , Mrs. K.C : Kumkum Chaudhary, Er. P.K. : Pankaj kumar, Er. P.V: Parul Varshney, Er. S.S : Shobhit Saxena, Er. AS : Archana Sharma, Er. RP: Rakesh Pandey, mini project : All faculty
Tuesday	Electronic Switching (Er. P.V)	Digital Signal Processing (Dr. P.K.)	VLSI Technology (Er. V.R.)	Integrated Circuits (Er. R.P.)		DSP Lab G2 (Er. P.K.)	DSP Lab G2 (Er. P.K.)	
Wednesday	Electronic Switching (Er. P.V)	Microprocessor& Microcontroller (Er. A.S.)	VLSI Technology (Er. V.R.)	Integrated Circuits (Er. R.P.)		Microprocessor& Microcontroller Lab G1 (Er. V. R)	Microprocessor& Microcontroller Lab G1 (Er. V. R)	
Thursday	Electronic Switching (Er. P.V)	Microprocessor& Microcontroller (Er. A.S.)	VLSI Technology (Er. V.R.)	Integrated Circuits (Er. R.P.)		Microprocessor& Microcontroller Lab G2 (Er. V. R)	Microprocessor& Microcontroller Lab G2 (Er. V. R)	
Friday	Digital Signal Processing (Dr. P.K.)	Microprocessor& Microcontroller (Er. A.S.)	ITCS (Guest faculty)	mini project		IC Lab G1	IC Lab G1	
Saturday	Digital Signal Processing (Dr. P.K.)	Microprocessor& Microcontroller (Er. A.S.)	ITCS (Guest faculty)	mini project		IC Lab G2	IC Lab G2	

SIR CHHOTU RAM INSTITUTE OF ENGG. & TECH.

TIME-TABLE

Deptt. : Electronics & Communication Engg.

Semester : 7th

Class Counselor: Er. V. Ram

From: To:	09:30 AM to 10:30 AM	10:30 AM to 11:30 AM	11:30 AM to 12:30 PM	12:30 PM to 01:30 PM	01:30 PM to 02:30 PM	02:30 PM to 03:30 PM	03:30 PM to 04:30 PM	
Monday	VLSI Design (Er. V.R)	Information Theory & Coding Tutorial (Dr. S.S)	VLSI Design Lab (Er. V.R)(G1)	VLSI Design Lab (Er. V.R)(G1)	LUNCH	Project	Project	Er. V.R. : V.K. Ram , Mrs. K.C : Kumkum Chaudhary, Er. P.S. : Priyanka Singh, Dr. S.S : Shobhit Saxena. Project :: All faculty members of EC Dept.
Tuesday	VLSI Design (Er. V.R)	Information Theory & Coding Tutorial (Dr. S.S)	VLSI Design Lab (Er. V.R)(G2)	VLSI Design Lab (Er. V.R)(G2)		Project	Project	
Wednesday	Industrial Training	Information Theory & Coding Tutorial (Dr. S.S)	Project	Project		PD	Project	
Thursday	Industrial Training	PME (Dr. Kumkum)	Project	Project		VLSI Design Tutorial (Er. V.R)	RES Tutorial (Er. PS)	
Friday	RES (Er. PS)	PME (Dr. Kumkum)	Project	Project		PD	Project	
Saturday	RES (Er. PS)	PME tutorial (Dr. Kumkum)	Project	Project		Project	Project	